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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Praveen Parvathala

Examiner: Tabone, John

Patent No.: 6,948,096

Group Art Unit: 2133

Issue Date: September 20, 2005

Docket No: 80107.051US1

Title: FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES
SUCH AS MICROPROCESSORS

Certificate

OCT 19 2005

of Correction

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

Customer Number 45445:

By:

Dana B LeMoine

Name: Dana B. LeMoine

Reg. No.40,062

DBL/clh

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Chris Hammond

Name

Chris Hammond

Signature

OCT 21 2005



Patent 6,948,096

PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,948,096

Docket No: 80107.051US1

Issue Date: September 20, 2005

Patentee: Praveen Parvathala

Title

**FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR
COMPLEX DEVICES SUCH AS MICROPROCESSORS**

REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

Respectfully Submitted

PRAVEEN PARVATHALA

By his Representatives,

Customer Number 45445

Telephone Number 952-473-8800

Date: 10-12-05

By: Dana B. LeMoine

Dana B. LeMoine

Reg. No: 40,062

DBL/clh

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 12 day of October 2005

Chris Hammond
Name

Chris Hammond
Signature

OCT 21 2005

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,948,096
DATED : September 20, 2005
INVENTOR(S) : Parvathala

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, line 43, in Claim 3, after "comprises" delete "," and insert - - ; - -, therefor.

In column 10, line 51, in Claim 12, after "tests" insert - - ; - -.

In column 10, line 67, in Claim 14, after "following" delete "," and insert - - ; - -, therefor.

In column 11, line 25, in Claim 17, after "microprocessor" insert - - , - -.

In column 12, line 2, in Claim 17, after "microprocessor" insert - - , - -.

In column 12, line 5, in Claim 17, delete "memo" and insert - - memory - -, therefor.

MAILING ADDRESS OF SENDER:

LeMoine Patent Services, PLLC
C/O PortfolioIP
P.O. Box 52050
Minneapolis, MN 55402

Atty Docket No: 80107.051US1

PATENT NO. 6,948,096

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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In column 11, line 25, in Claim 17, after "microprocessor" insert - - , - - .

In column 12, line 2, in Claim 17, after "microprocessor" insert - - , - - .

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